



SEL-2410 I/O Processor Guideform Specification

The microprocessor-based I/O processor shall provide monitoring, control, and automation. Self-checking functions shall be included. Specific requirements are as follows:

SELOGIC Programming Language. The I/O processor shall be capable of implementing a wide variety of logic and control functions using the tools available in the SELOGIC Programming Language.

Small Form Factor. A compact case with quick disconnect connectors for analog and digital I/O shall be used to simplify installation.

Flexible I/O. Configure the I/O processor based upon end user application requirements.

Analog Inputs. As an option the I/O processor shall have the ability to support eight ± 1 , ± 2.5 , ± 5 , ± 10 , ± 20 , +4 to 20mA or ± 1 , ± 2.5 , ± 5 , or ± 10 Vdc (field selectable) analog inputs.

IRIG-B Synchronized, Time-Stamped Events. Up to 512 event records with IRIG-B synchronized time stamps shall be available. An internal real time clock shall be used for time stamping if an IRIG-B signal is not available.

Sequential Event Recorder. A chronological report shall be provided by the I/O processor to help determine the order and cause of events and assist in troubleshooting. The last 512 input, output, and element events shall be recorded.

The module shall have an operating temperature range of -40° to $+85^{\circ}\text{C}$ (-40° to $+185^{\circ}\text{F}$) and a power supply input operating voltage range of 85–264 Vac/85–275 Vdc.

The front panel shall meet NEMA 12/IP54.

PGS2410-01